

Amendments to the Claims:

This listing of claims will replace all prior versions and listings of claims in the application.

Listing of Claims:

1.-23. (Canceled)

24. (Currently amended) ~~The~~ An in-band-flat-group-delay type dielectric filter, ~~according to claim 23 comprising:~~

a plurality of dielectric resonators,

a main circuit formed of series coupling capacitors, with which the dielectric resonators are coupled to one another; and

an auxiliary circuit for coupling the main circuit to capacitors by bypass coupling, wherein both deviations in group delay time and in amplitude between input/output terminals fall within specified certain deviation values, respectively, at the same time at a center frequency and within a specified frequency band around the center frequency,

wherein the auxiliary circuit includes parallel bypass capacitors and series bypass capacitors;

two of the series coupling capacitors connect between the adjacent dielectric resonators;

each one end of the parallel bypass capacitors is connected to a junction between the two of the series coupling capacitors; and

the other ends of the adjacent parallel bypass capacitors are connected to be short circuited or via at least one of the series bypass capacitors.

25. (Currently amended) ~~The~~ An in-band-flat-group-delay type dielectric filter ~~according to claim 23, comprising:~~

a plurality of dielectric resonators,

a main circuit formed of series coupling capacitors, with which the dielectric resonators are coupled to one another; and

an auxiliary circuit for coupling the main circuit to capacitors by bypass coupling,

wherein both deviations in group delay time and in amplitude between input/output terminals fall within specified certain deviation values, respectively, at the same time at a center frequency and within a specified frequency band around the center frequency,

~~wherein~~ the auxiliary circuit includes parallel bypass capacitors and series bypass capacitors;

one of the series coupling capacitors connects between the adjacent dielectric resonators;
each one end of the parallel bypass capacitors is connected to a junction between the series coupling capacitors; and

the other ends of the adjacent parallel bypass capacitors are connected to be short circuited or via at least one of the series bypass capacitors.

26. (Previously presented) The in-band-flat-group-delay type dielectric filter according to claim 24, wherein at least one of the parallel bypass capacitors is opened.

27. (Previously presented) The in-band-flat-group-delay type dielectric filter according to claim 24, wherein at least one of the series bypass capacitors is short circuited.

28. (Currently amended) ~~The~~ An in-band-flat-group-delay type dielectric filter according to ~~claim 23, comprising:~~

a plurality of dielectric resonators,

a main circuit formed of series coupling capacitors, with which the dielectric resonators are coupled to one another; and

an auxiliary circuit for coupling the main circuit to capacitors by bypass coupling,

wherein both deviations in group delay time and in amplitude between input/output terminals fall within specified certain deviation values, respectively, at the same time at a center frequency and within a specified frequency band around the center frequency,

wherein frequency characteristics in group delay have a peak value at a lower edge of a passband in amplitude transfer characteristics and uniform-group-delay frequency characteristics within the passband; and

in a higher frequency band than an upper edge of the passband, the frequency characteristics in group delay frequency characteristics do not increase from a uniform group delay time within the passband but decrease.

29. (Currently amended) ~~A linearized amplifier, including a~~ An in-band-flat-group-delay type dielectric filter according to claim 23, comprising:

a plurality of dielectric resonators,

a main circuit formed of series coupling capacitors, with which the dielectric resonators are coupled to one another; and

an auxiliary circuit for coupling the main circuit to capacitors by bypass coupling,

wherein both deviations in group delay time and in amplitude between input/output terminals fall within specified certain deviation values, respectively, at the same time at a center frequency and within a specified frequency band around the center frequency, and

~~wherein~~ a group delay time in a distortion compensating circuit is regulated by the dielectric filter.

30. (Original) The linearized amplifier according to claim 29, wherein the distortion compensating circuit is a feedforward-type distortion compensating circuit.

31. (Original) The linearized amplifier according to claim 29, wherein a uniform-group-delay frequency band width in the dielectric filter is at least three times as wide as a bandwidth required for the linearized amplifier.

32. (Original) A linearized amplifier, including a dielectric filter according to claim 26, wherein a group delay time in a distortion compensating circuit is regulated by the dielectric filter.

33. (Original) The linearized amplifier according to claim 32, wherein the distortion compensating circuit is a feedforward-type distortion compensating circuit.

34. (Original) The linearized amplifier according to claim 32, wherein a uniform-group-delay frequency band width in the in-band-flat-group-delay type dielectric filter is at least three times as wide as a bandwidth required for the linearized amplifier.

35. (Original) A linearized amplifier, including a dielectric filter according to claim 27, wherein a group delay time in a distortion compensating circuit is regulated by the dielectric filter.

36. (Original) The linearized amplifier according to claim 35, wherein the distortion compensating circuit is a feedforward-type distortion compensating circuit.

37. (Original) The linearized amplifier according to claim 35, wherein a uniform-group-delay frequency band width in the in-band-flat-group-delay type dielectric filter is at least three times as wide as a bandwidth required for the linearized amplifier.

38. (Original) A linearized amplifier, including a dielectric filter according to claim 28, wherein a group delay time in a distortion compensating circuit is regulated by the dielectric filter.

39. (Original) The linearized amplifier according to claim 38, wherein the distortion compensating circuit is a feedforward-type distortion compensating circuit.

40. (Original) The linearized amplifier according to claim 38, wherein a uniform-group-delay frequency band width in the in-band-flat-group-delay type dielectric filter is at least three times as wide as a bandwidth required for the linearized amplifier.

41. (New) The in-band-flat-group-delay type dielectric filter according to claim 25, wherein at least one of the parallel bypass capacitors is opened.

42. (New) A linearized amplifier, including a dielectric filter according to claim 41, wherein a group delay time in a distortion compensating circuit is regulated by the dielectric filter.

43. (New) The linearized amplifier according to claim 42, wherein the distortion compensating circuit is a feedforward-type distortion compensating circuit.

44. (New) The linearized amplifier according to claim 42, wherein a uniform-group-delay frequency band width in the in-band-flat-group-delay type dielectric filter is at least three times as wide as a bandwidth required for the linearized amplifier.

45. (New) The in-band-flat-group-delay type dielectric filter according to claim 25, wherein at least one of the series bypass capacitors is short circuited.

46. (New) A linearized amplifier, including a dielectric filter according to claim 45, wherein a group delay time in a distortion compensating circuit is regulated by the dielectric filter.

47. (New) The linearized amplifier according to claim 46, wherein the distortion compensating circuit is a feedforward-type distortion compensating circuit.

48. (New) The linearized amplifier according to claim 46, wherein a uniform-group-delay frequency band width in the in-band-flat-group-delay type dielectric filter is at least three times as wide as a bandwidth required for the linearized amplifier.

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